

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. DATUMTE.015A	APPLICATION NO. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANTS Taylor, et. al.	
		FILING DATE Herewith	GROUP ART UNIT Unknown

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
HBP		5,905,996	05/18/99	Pawlowski			
HBP		5,905,997	05/18/99	Stiles			

FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
HBP	Baer, J., "Computer Systems Architecture," Computer Science Press, Inc., pp. 306-307 (1980).
HBP	Chandrakasan, A., Bowhill, W., and Fox, F., "Design of High-Performance Microprocessor Circuits," The Institute of Electrical and Electronics Engineers, Inc., IEEE Press, pg. 287 (2001).

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EXAMINER H.B. Patel	DATE CONSIDERED 12/06/2005
*EXAMINER: INITIAL IF CITATION CONSIDERED. WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	